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INFORMATION CITED BY APPLICANT  (Use several sheets if necessary)		Applicant Mandelman et al.	
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U.S. Patent Documents						
Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if appropriate
DLN	5,557,135	9/17/96	Hashimoto	257	308	6/14/94

Foreign Patent Documents						
	Document Number	Date	Country	Class	Subclass	Translation

Other Prior Art (including Author, Title, Date, Pertinent Pages, etc.)	
DLN	"Effects of a New Trench-Isolated Transistor Using Sidewall Gates" by Hieda et al., from IEEE Transactions of Electron Devices, Vol. 36, No. 9, Dated September 1989, pgs. 1615-1619
Examiner	Date Considered
<i>Richard Nyman</i>	11/28/01